

U.S. Department of Commerce, Patent and Trademark Office					Atty Docket No.		Serial No.	
					9145.0008		09/347,106	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicant(s)			
(Use several sheets if necessary)					Hronik, Stanley A.			
					Filing Date		Group	
					April 22, 2003		2186	
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
MMA		5,638,335	6/10/1997	Akiyama et al.	365	230.03		
		5,022,011	6/4/1991	Allan	365	283		
		5,644,729	7/1/1997	Amini et al.	395	250		
		5,659,696	8/19/1997	Amini et al.	395	412		
		3,967,247	6/29/1976	Andersen et al.	340	172.5		
		5,170,074	12/8/1992	Aoki	307	279		
		5,590,073	12/31/1996	Arakawa et al.	365	185.08		
		4,426,681	1/17/1984	Bacot et al.	364	200		
		5,122,690	6/16/1992	Bianchi	307	475		
		5,134,311	7/28/1992	Biber et al.	307	270		
		4,638,187	1/20/1987	Boler et al.	307	451		
		6,151,236	11/21/00	Bondurant et al.	365	49		
		5,319,606	6/7/1994	Bowen et al.	365	230.06		
		5,561,781	10/1/1996	Braceras et al.	395	458		
		4,371,929	2/1/1983	Brann et al.	364	200		
		6,167,487	12/26/2000	Camacho et al.	711	131		
		5,274,276	12/28/1993	Casper et al.	307	443		
		5,278,460	1/11/1994	Casper	307	296.5		
		5,311,481	5/10/1994	Casper et al.	365	230.06		
		5,347,179	9/13/1994	Casper et al.	307	451		
		5,361,002	11/1/1994	Casper	327	530		
		5,819,060	10/6/1998	Cesana	395	395		
		5,128,560	7/7/1992	Chern et al.	307	475		
		5,384,737	1/24/95	Childs et al.	365	189.05		
	4,884,270	11/28/1989	Chiu et al.	371	21.2			
	4,912,630	3/27/90	Cochcroft, jr.	364	200			
	5,508,638	4/16/1996	Cowles et al.	326	38			
	5,581,734	12/3/1996	DiBrino et al.	395	496			

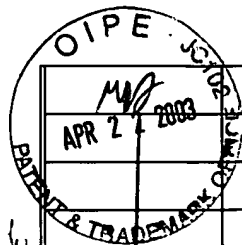
RECEIVED

APR 28 2003

Technology Center 2100

MMA 6/2/03

09347106



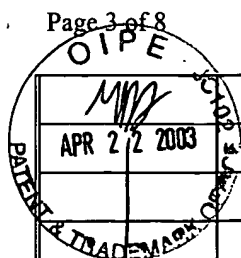
4,404,474	9/13/1983	Dingwall	307	260	
4,490,782	12/25/1984	Dixon et al.	364	200	
4,476,526	10/9/1984	Dodd	364	200	
5,777,942	11/19/89	Dosaka et al.	365	230.03	
5,194,765	3/16/1993	Dunlop et al.	307	443	
5,050,072	9/17/1991	Earnshaw et al.	364	200	
5,471,591	11/28/1995	Edmondson et al.	395	375	
5,717,904	2/10/1998	Ehlers et al.	395	511	
5,754,815	5/19/1998	Ernst et al.	395	405	
4,611,337	9/9/1986	Evans	377	123	
4,958,088	9/18/1990	Farah-Bakhsh et al.	307	443	
5,473,575	12/5/1995	Farmwald et al.	365	280.06	
5,513,327	4/30/1996	Farmwald et al.	395	309	
6,314,051	11/6/01	Farmwald et al.	365	233	
5,576,645	11/19/1996	Farwell	327	94	
5,546,344	8/13/1996	Fawcett	365	189.05	
4,789,796	12/6/1988	Foss	307	443	
5,668,763	9/16/1997	Fujioka et al.	365	200	
5,341,341	8/94	Fukuzo	365	233	
5,636,163	6/3/1997	Furutani et al.	365	189.01	
5,572,467	11/5/96	Ghassemi et al.	365	189.07	
5,043,937	8/27/91	Glaise			
5,581,718	12/3/96	Grochowski			
5,757,704	5/26/98	Hachiya	365	189.07	
4,442,488	4/10/1984	Hall	364	200	
5,694,065	12/2/1997	Hamasaki et al.	327	108	
4,530,054	7/16/1985	Hamstra et al.	364	200	
4,530,055	7/16/1985	Hamstra et al.	364	200	
4,433,374	2/21/1984	Hanson et al.	364	200	
4,423,479	12/27/1983	Hanson et al.	364	200	
5,440,260	8/8/1995	Hayashi et al.	327	278	
5,165,046	11/17/1992	Hesson	307	270	
5,179,298	1/12/1993	Hirano et al.	307	443	
5,321,368	6/14/1994	Hoelzle	328	63	
5,254,883	10/19/1993	Horowitz et al.	307	443	
5,619,473	4/8/1997	Hotta	365	238.5	
6,052,769	4/18/2000	Huff et al.	712	3	
5,394,555	2/28/1995	Hunter et al.	395	800	

RECEIVED

APR 28 2003

Technology Center 2100

6/2/03



5,506,814	4/9/1996	Hush et al.	365	230.03	
5,128,563	7/7/1992	Hush et al.	307	482	
5,790,838	8/4/98	Irish			
5,781,481	7/14/1998	Iwakiri	365	189.11	
5,933,385	8/99	Jiang et al.	365	230.1	
5,577,236	11/19/1996	Johnson et al.	395	551	
5,621,690	4/15/1997	Jungroth et al.	365	200	
5,467,473	11/14/1995	Kahle et al.	395	800	
4,695,943	9/22/1987	Keeley et al.	364	200	
5,870,347	2/9/1999	Keeth et al.	365	230.03	
5,488,712	1/30/96	Kiuchi	395	431	
5,384,745	1/24/95	Konishi et al.	365	230.03	
6,259,648	7/10/01	Kragick	365	230.05	
4,928,281	5/22/1990	Kurosawa et al.	371	51.1	
5,276,642	1/4/1994	Lee	365	189.04	
5,498,990	3/12/1996	Leung et al.	327	323	
5,761,147	6/2/1998	Lindner et al.	365	230.05	
5,347,177	9/13/1994	Lipp	307	443	
5,875,152	2/23/1999	Liu et al.	365	233.5	
5,650,971	7/22/1997	Longway et al.	365	207	
5,627,780	5/6/1997	Malhi	365	185.09	
5,652,724	7/29/97	Manning	365	189.05	
5,831,929	11/3/1998	Manning	365	233	
5,825,711	10/20/1998	Manning	365	230.03	
5,655,105	8/5/1997	McLaury	395	496	
5,793,688	8/11/98	McLaury	365	203	
5,349,566	9/20/1994	Merritt et al.	365	233.5	
6,094,399	6/25/00	Mick			
5,841,732	11/24/98	Mick	365	233	
6,249,480 B1	06/19/01	Mick	365	233	
5,828,606	10/27/98	Mick	365	189.05	
5,838,631	11/17/98	Mick	365	233	
5,875,151	02/23/99	Mick	365	233	
4,410,942	10/18/1983	Milligan et al.	364	200	
5,111,435	5/5/1992	Miyamoto	365	230.06	
5,483,497	1/9/1996	Mochizuki et al.	365	230.03	
5,321,651	6/14/94	Monk	365	189.01	
5,617,362	4/1/1997	Mori et al.	365	189.05	
5,581,197	12/3/1996	Motley et al.	326	30	

RECEIVED

APR 28 2003

Technology Center 2100

Mander 6/2/03

09347166

PATENT & TRADEMARK OFFICE APR 22 2003	4,942,550	7/17/1990	Murray	364	900	
	5,631,872	5/20/1997	Naritake et al.	365	227	
	4,547,848	10/15/1985	Nishida et al.	364	200	
	5,377,338	12/27/1994	Olson et al.	395	375	
	5,487,035	1/23/96	Nishimura et al.	365	189.02	
	5,781,480	7/14/98	Nogle et al.	365	189.04	
	5,917,772	6/29/1999	Pawlowski	365	230.06	
	5,675,549	10/7/97	Ong et al.	365		
	6,058,448	5/2/2000	Pawlowski	710	107	
	6,438,066	8/20/02	Ooishi et al.	365	233	
	5,502,676	3/26/96	Pelley III et al.	365	200	
	6,256,716	7/3/01	Pham	711	167	
	5,383,157	1/17/1995	Phelan	365	201	
	4,796,231	1/3/89	Pinkham	365	189	
	4,817,058	3/28/1989	Pinkham	365	230	
	5,195,056	3/16/1993	Pinkham et al.	365	189.05	
	5,150,186	9/22/1992	Pinney et al.	357	42	
	4,225,922	9/30/1980	Porter	364	200	
	4,208,716	6/17/1980	Porter et al.	364	200	
	6,212,109	4/3/01	Proebsting	365	190	
	5,400,283	3/21/1995	Raad	365	203	
	5,574,698	11/12/1996	Raad	365	230.06	
	5,636,174	6/3/1997	Rao	365	230.03	
	5,890,195	3/30/1999	Rao	711	105	
	5,619,453	4/8/97	Roohparvar et al.	365	185.33	
	6,078,527	6/20/2000	Roth et al.	365	189.04	
	6,044,429	03/28/00	Ryan et al.	710	131	
	5,699,317	12/16/97	Sartore et al.	365	230.06	
	4,984,204	1/8/1991	Sato et al.	365	189.11	
	5,568,077	10/22/1996	Sato et al.	327	199	
	5,497,127	3/5/1996	Sauer	331	17	
	4,437,155	3/13/1984	Sawyer et al.	364	200	
	5,636,173	6/3/1997	Schaefer	365	230.03	
	5,220,208	6/15/1993	Schenck	307	443	
	4,096,402	6/20/1978	Schroeder et al.	307	362	
	5,578,941	11/26/1996	Sher et al.	326	34	
	5,737,276	4/7/1998	Shin et al.	365	230.08	
	5,457,407	10/10/1995	Shu et al.	326	30	
	5,438,545	8/1/1995	Sim	365	189.05	

RECEIVED

APR 28 2003

Technology Center 2100

M. Sullivan 6/2/03

RECEIVED
APR 28 2003
Technology Center 2100

Foreign Patent Documents

Muller 6/6/03

09347106


							Translation	
Document	Date	Country	Class	Subclass	Yes	No		
EP 0294287 B1	12/07/1988	Europe	G11B	20/18	X			
EP 0432509 A2	6/19/1991	Europe	G11C	7/00	X			
EP 0276871 A2	8/3/1988	Europe	G11C	8/00	X			
EP 0450871 A2	10/9/1991	Europe			X			
EP 0655741 A2	5/31/1995	Europe			X			
EP 0680049 A2	11/2/1995	Europe			X			
EP 0692872 A2	1/17/1996	Europe			X			
EP 1 130 603	9/5/01	Europe						
WO 97/03445	1/30/1997	PCT			X			
WO 97/14289	4/24/1997	PCT			X			
WO 97/15055	4/27/1997	PCT			X			
WO 94/29871	12/22/1994	PCT			X			
JP 2-112317	4/25/1990	Japan (+ Abstract)				X		
JP 4-135311	8/5/1992	Japan (+ Abstract)				X		
JP 5-136664	1/6/1993	Japan (+ Abstract)				X		
JP 5-282868	10/29/1993	Japan				X		
						</		

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

MPB	Abstract of "Synchronous SRAMs having zero bus turnaround time," Elektronik Industrie, December 1996, 1 p.
	Bapat, Shekhar, "Synthesizable 200 MHz ZBT SRAM Interface," Xilinx, January 10, 2000, pp. 1-6.
	Bennett, Steve, "SRAMs: if synchronous just isn't enough," Electronic Product Design, July 1997, pp. 29-30.
	Computer Design, "Application-specific memory evolves," December 1998, pp. 1-4.
	Cypress Semiconductor Corporation, "SRAMs & MODULES: NoBL™ SRAMs", 2001, pp. 1-2.
	Cypress Semiconductor Corporation, "NoBL™ SRAM Fact Sheet," pp. 1-2.
	Cypress Semiconductor Corporation, "Introduction to Cypress SRAMs," April 20, 2001, pp. 1-3.
	Cypress Semiconductor Corporation, "NoBL: The Fast SRAM Architecture," June 29, 1998, pp. 1-8.


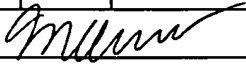
 6/2/03

09347106

	Cypress Semiconductor Corporation, "NoBL™, The ZBT™-Compatible...", June 26, 1998, pp. 1-3.
	Descriptive literature entitled, "400 MHz SLD RAM, 4M x 16 SLD RAM Pipelined, Eight Bank, 2.5. V Operation," SLD RAM Consortium Advance Sheet, published throughout the U.S., pp. 1-22.
	Dipert, Brian, "No-latency SRAMs tackle fast-changing data," Cypress Semiconductor, 1997, pp. 1-2.
	"Draft Standard for a High-Speed Memory Interface (SyncLink)," Microprocessor and Microcomputer Standards Subcommittee of the IEEE Computer Society, Copyright 1996 by the Institute of Electrical and Electronic Engineers. Inc., New York. NY. pp. 1-56.
	GSI Technology, "Company Overview," August 2001, pp.1-5.
	IBM Preliminary, IBM043611RLAB, IBM041811RLAB, 32K X 36; & 64K X 18 SRAM, <i>IBM Corporation</i> (1996), pp. 1-20.
	IBM Corporation Datasheet, "IBM043610QLAB, IBM041810QLAB: 32K x 36 & 64K x 18 SRAM," July, 1996, pp. 1-21.
	IBM Corporation Datasheet, "IBM043611QLAB, IBM041811QLAB: 32K x 36 & 64K x 18 SRAM," July, 1996, pp. 1-21.
	Integrated Device Technology, Inc., "IDT Introduces Industry's Fastest 4-Mbit Synchronous ZBT SRAM," March 30, 1998, pp. 1-3.
	Integrated Device Technology, Inc., "128K x 36, 3.3V Synchronous SRAM with ZBT™ Feature, Burst Counter and Pipelined Outputs," December 1999, pp. 1-20.
	Integrated Device Technology, Inc., "IDT Introduces Revolutionary ZBT™ Synchronous SRAM Architecture," 1996, pp. 1-3.
	Integrated Device Technology, Inc., "Self-Timed BiCMOS ECL Static RAM: 64K (16K x 4-BIT) STRAM", August 1992, pp. 1-8.
	Matsumoto, Craig, "Cypress Details Product Road Map," November 17, 1998, pp. 1-2.
	Integrated Device Technology, Inc., News Release in Japanese, September 15, 1997, pp, 1-2, no translation provided.
	MacLellan, Andrew, "Rivals To Cooperate On SRAM Project," July 26, 1999, pp. 1-2.
	Motorola, "Dual Differential Clock Synchronous FSRAM with Very Late Write and Asynchronous /G", Print Date: July 9, 1993, Rev. 1.03, pp. 13-16.
	Motorola, "Semiconductor Technical Data: MCM69L738 & MCM69L820: Advance Information: \$M Late Write 2.5 V I/O," 15 pp.
	Motorola, "Semiconductor Technical Data: MCM62486: Product Review: 32K x 9 Bit BurstRAM™ Synchronous Static RAM With Burst Counter and Self-Timed Write," 1991, pp. 1-4, 6, 8-9.
	Motorola, Inc. "Semiconductor Technical Data: The Motorola BurstRAM™," November 17, 1999, pp. 1-4.

 6/2/03

09347106

		Prince, <u>Semiconductor Memories: A Handbook of Design, Manufacture and Application</u> , 2d ed., 1991, pp. 467-472.
		Semiconductor Business News, "Micron Samples 8-Mbit Smart ZBT SRAM," October 21, 1999, 1 p.
		SONY CXK77B3611 Advanced Information, "32,768-WORD by 36-BIT HIGH SPEED BiCMOS SYNCHRONOUS STATIC RAM", December 10, 1994, pp. 1, 3, and 9).
		Sweazey, Paul, "SRAM Organization, Control, and Speed, and their Effect on Cache Memory Design," 1987, pp. 434-437.
		Taguchi, M. et al., "A 40-ns 64-Mb DRAM with 64-b Parallel Data Bus Architecture," IEEE Journal of Solid-State Circuits, Vol 26, November 1991, pp. 1493-1497.
		Will Wade, "IDT and Micron team up for smart ZBT SRAM," EE Times, August 25, 1999, 1 p.
Examiner 	Date Considered <u>6/2/03</u>	
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.		

RECEIVED

APR 28 2003

Technology Center 2100